

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Gary F. Feierbach

Attorney Docket No.: APL1P203

Application No.: New

Examiner: Not Yet Assigned

Filed: Herewith

Group: Not Yet Assigned

Title: METHOD AND APPARATUS FOR
SAVING POWER IN PIPELINED PROCESSORS

JC972 U.S. PTO
09/814321
03/21/01

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §§1.56 AND 1.97(c)

Commissioner for Patents
Box Patent Application
Washington, DC 20231

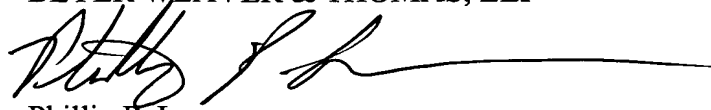
Dear Sir:

The references listed in the attached PTO Form 1449, copies of which are attached, may be material to examination of the above-identified patent application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these references of official record in this application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

This Information Disclosure Statement is being filed within three (3) months of the filing date of the above-referenced application. Accordingly, it is believed that no fees are due in connection with the filing of this Information Disclosure Statement. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 500388 (Order No. APL1P203).

Respectfully submitted,
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